

## **ABSTRACT OF THE DISCLOSURE**

A control interface card is constructed to include a bus controller connected to the CPU of a host computer, a data buffer adapted for registration of object position data computed by the CPU of the host computer, a position compare circuit adapted for fetching registered object position data from the data buffer and comparing the fetched object position data with feedback position data obtained from an object shifting control means, and then fetching a next registered object position data from the data buffer for a next comparison after matching of one comparison, and a trigger I/O circuit adapted for providing a triggering signal to the CPU of the host computer upon matching of one comparison at the position compare circuit.